

Figure 15 shows the detail of an OCR register of a preferred embodiment according to the present invention.

Figure 16 shows the format of a CID register of a preferred embodiment according to the present invention.

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Figures 17A + 17B show

~~Figure 17~~ shows the format of a CSD register of a preferred embodiment according to the present invention.

Figure 18 shows the format of the CSD register structure field of the CSD register of a preferred embodiment according to the present invention.

10 Figure 19 shows the format of the MultiMediaCard protocol version field of the CSD register of a preferred embodiment according to the present invention.

Figure 20 shows the format of the TAAC field of the CSD register of a preferred embodiment according to the present invention.

15 Figure 21 shows the format of the TRAN_SPEED field of the CSD register of a preferred embodiment according to the present invention.

Figure 22 shows the format of the CCC field of the CSD register of a preferred embodiment according to the present invention.

Figure 23 shows the format of the BL_LEN field of the CSD register of a preferred embodiment according to the present invention.

20 Figure 24 shows the format of the DSR_IMP field of the CSD register of a preferred embodiment according to the present invention.

Figure 25 shows the format of the VDD_R_CURR_MIN, VDD_R_CURR_MAX, VDD_W_CURR_MIN, and VDD_W_CURR_MAX fields of the CSD register of a preferred embodiment according to the present invention.

25 Figure 26 shows the format of the R2W_FACTOR field of the CSD register of a preferred embodiment according to the present invention.

Figure 27 shows the format of the ECC field of the CSD register of a preferred embodiment according to the present invention.

Figure 28 shows a cross reference table of the CSD fields and different command classes of a preferred embodiment according to the present invention.

5 Figure 29 is a state diagram showing different modes of a MultiMediaCard system of a preferred embodiment according to the present invention.

Figure 30 shows a MultiMediaCard bus driver located in each of the MultiMediaCard cards of a preferred embodiment according to the present invention.

10 Figure 31 shows a timing diagram for a block write command of a preferred embodiment according to the present invention.

Figure 32 shows a timing diagram for a multiple block write command of a preferred embodiment according to the present invention.

15 Figure 33 shows a timing diagram for a stop transmission command issued during the data transfer from a host of a preferred embodiment according to the present invention.

Figure 34 shows a timing diagram for a stop transmission command issued during the transmission of a CRC status block of the preferred embodiment according to the present invention.

20 Figure 35 shows a timing diagram for a stop transmission command issued when a card is busy with writing the last data block of a preferred embodiment according to the present invention.

25 Figure 36 shows a timing diagram for a stop transmission command issued when a card is idle of a preferred embodiment according to the present invention.

Figure 37 shows a table illustrating different command classes of a preferred embodiment according to the present invention.

Figures 38A + 38B show
Figure 38 shows a table illustrating the basic commands and read stream commands (i.e. class 0 and class 1 commands) of a preferred embodiment according to the present invention.

Figure 39 shows a table illustrating the block oriented read commands (i.e. class 2 commands) of a preferred embodiment according to the present invention.

5 Figure 40 shows a table illustrating the stream write commands (i.e. class 3 commands) of a preferred embodiment according to the present invention.

Figure 41 shows a table illustrating the block oriented write commands (i.e. class 4 commands) of a preferred embodiment according to the present invention.

10 Figure 42 shows a table illustrating the group write protect commands (i.e. classes 6-8 commands) of a preferred embodiment according to the present invention.

Figure 43 shows a table illustrating the erase commands (i.e. class 5 commands) of a preferred embodiment according to the present invention.

15 Figure 44 shows a table illustrating the I/O mode commands (i.e. class 9 commands) of a preferred embodiment according to the present invention.

Figure 45 shows a detail description of a R1 response of a preferred embodiment according to the present invention.

Figure 46 shows a detail description of a R2 response of a preferred embodiment according to the present invention.

20 Figure 47 shows a detail description of a R3 response of a preferred embodiment according to the present invention.

Figure 48 shows a detail description of a R4 response of a preferred embodiment according to the present invention.

25 Figure 49 shows a detail description of a R5 response of a preferred embodiment according to the present invention.

Figures 50A & 50B show
Figure 50 shows a detail description of a response showing the status of a card of a preferred embodiment according to the present invention.

Figure 51 is a table showing all the timing diagram symbols of used in the preferred embodiment according to the present invention.